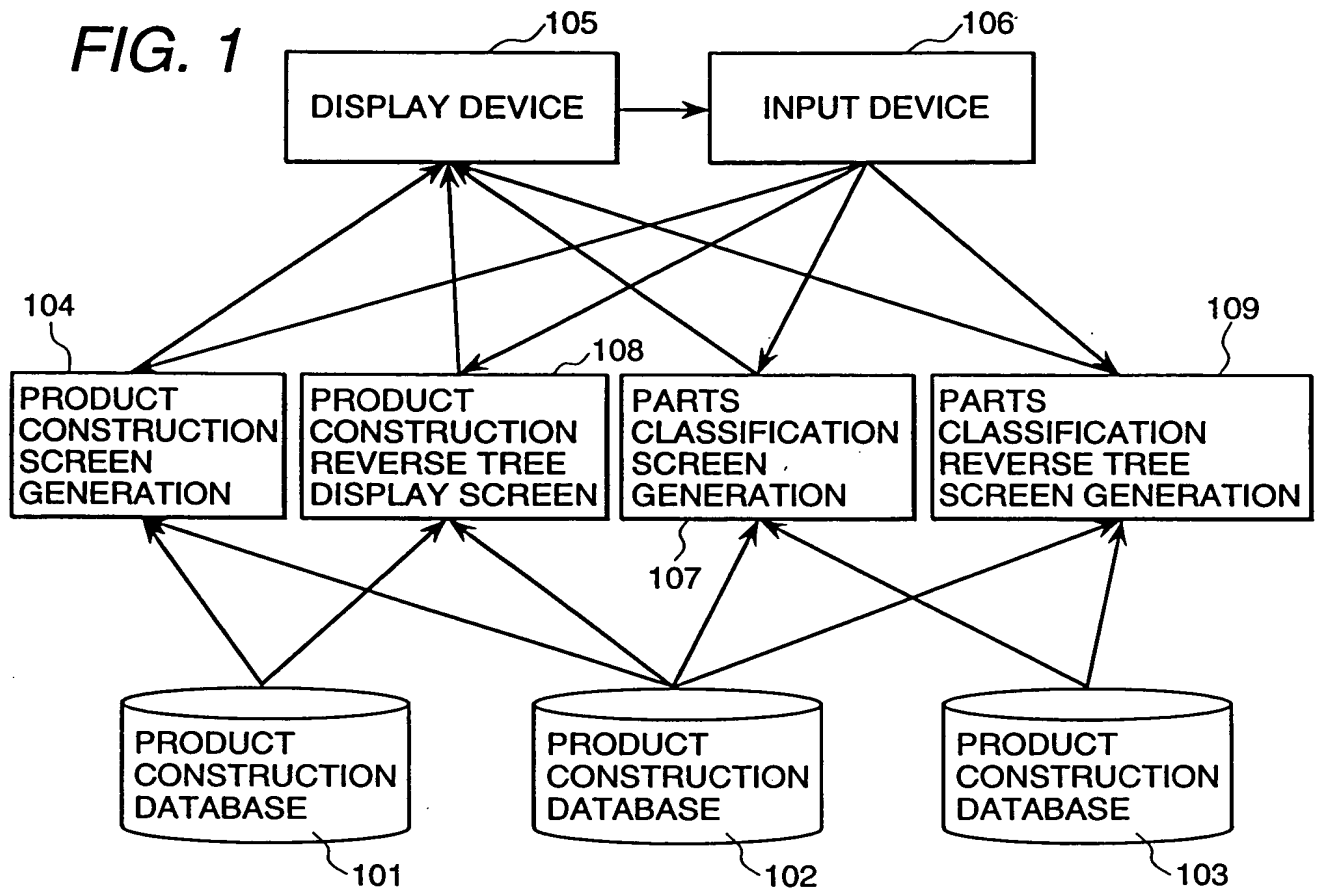


**FIG. 1****FIG. 2**

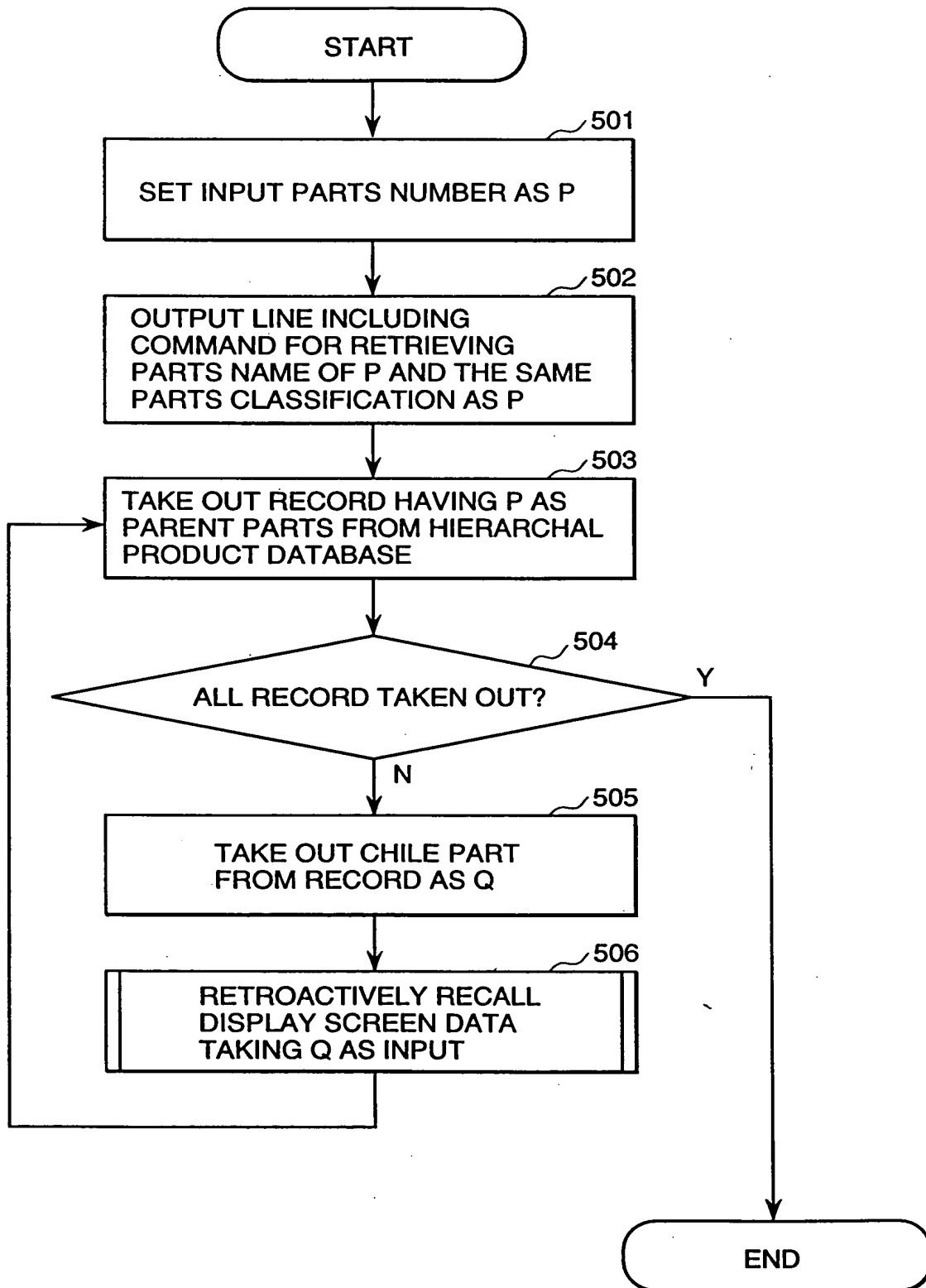
PARENT PARTS	CHILD PARTS	201
P1	P2	
P1	P3	
P2	P4	
P2	P5	
P11	P12	
P11	P13	
P12	P14	
P12	P15	

**FIG. 3**

PARENT PARTS	CHILD PARTS	
C1	C2	301
C1	C3	
C2	P4	
C2	P15	302
C2	P20	

**FIG. 4**

PARTS NO.	PARTS NAME	DRAWING	SPECIFICATION
P1	PRINTED BOARD ASSEMBLHY	D001	E001
P2	PRINTED BOARD	D002	E002
P3	SPACER	D003	—
P4	X100 150MHz	D004	E004
P5	CAPACITOR	—	E005
P11	PRINTED BOARD ASSEMBLHY	D006	E006
P12	CONTROL BOARD	D007	E007
P13	SPACER	D008	—
P14	CAPACITOR	—	E009
P15	X100 150MHz	D010	E010
P20	X110 100MHz	D011	E11

*FIG. 5*

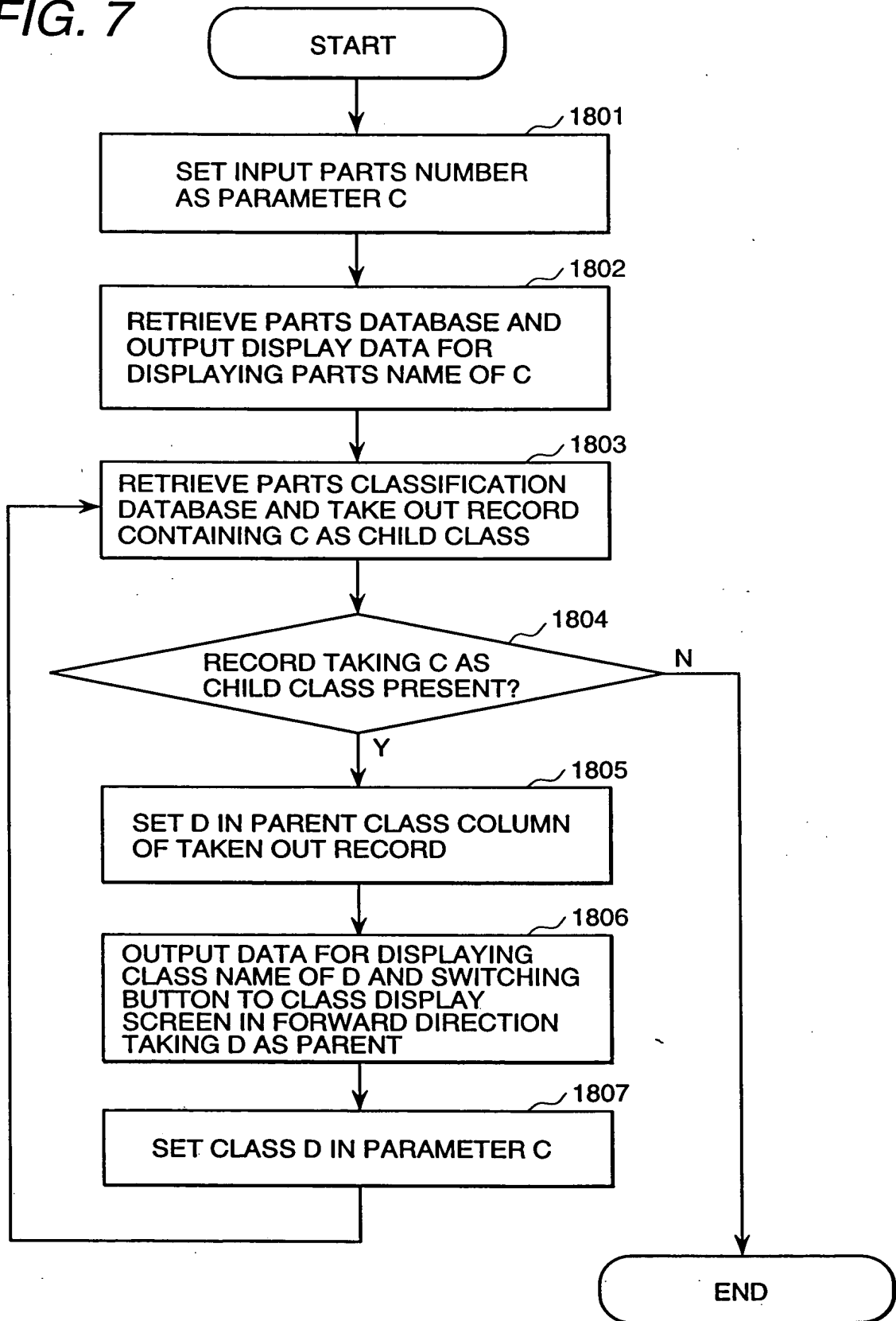
*FIG. 6*

PART NO.	PARTS NAME	DRAWING	SPECIFICATION
P1	PRINTED BOARD ASSY.	<u>CLASS</u> D001	<u>E001</u>
P2	PRINTED BOARD	<u>CLASS</u> D002	<u>E002</u>
P4	X100 100M	<u>CLASS</u> D004	<u>E004</u>
P5	CAPACITOR	<u>CLASS</u>	<u>E005</u>
P3	SPACER	<u>CLASS</u> D003	

*FIG. 8*

CLASSIFICATION	PARTS NAME/ CLASS NAME	USAGE	NORMAL CLASSIFICATION TREE
P4	X100 100MHz		
C2	CPU		
C1	ELECTRONIC PARTS		

FIG. 7



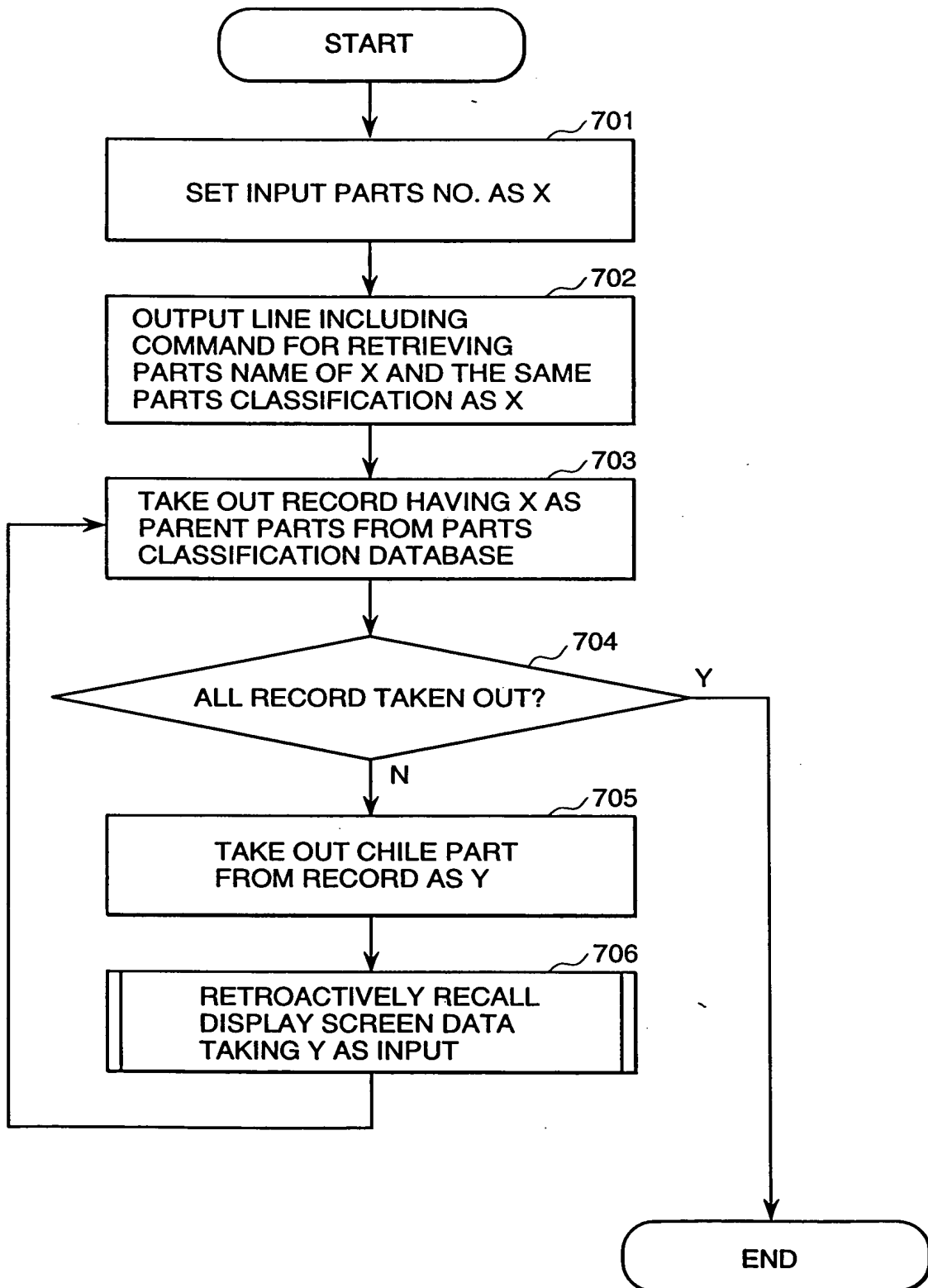
**FIG. 9**

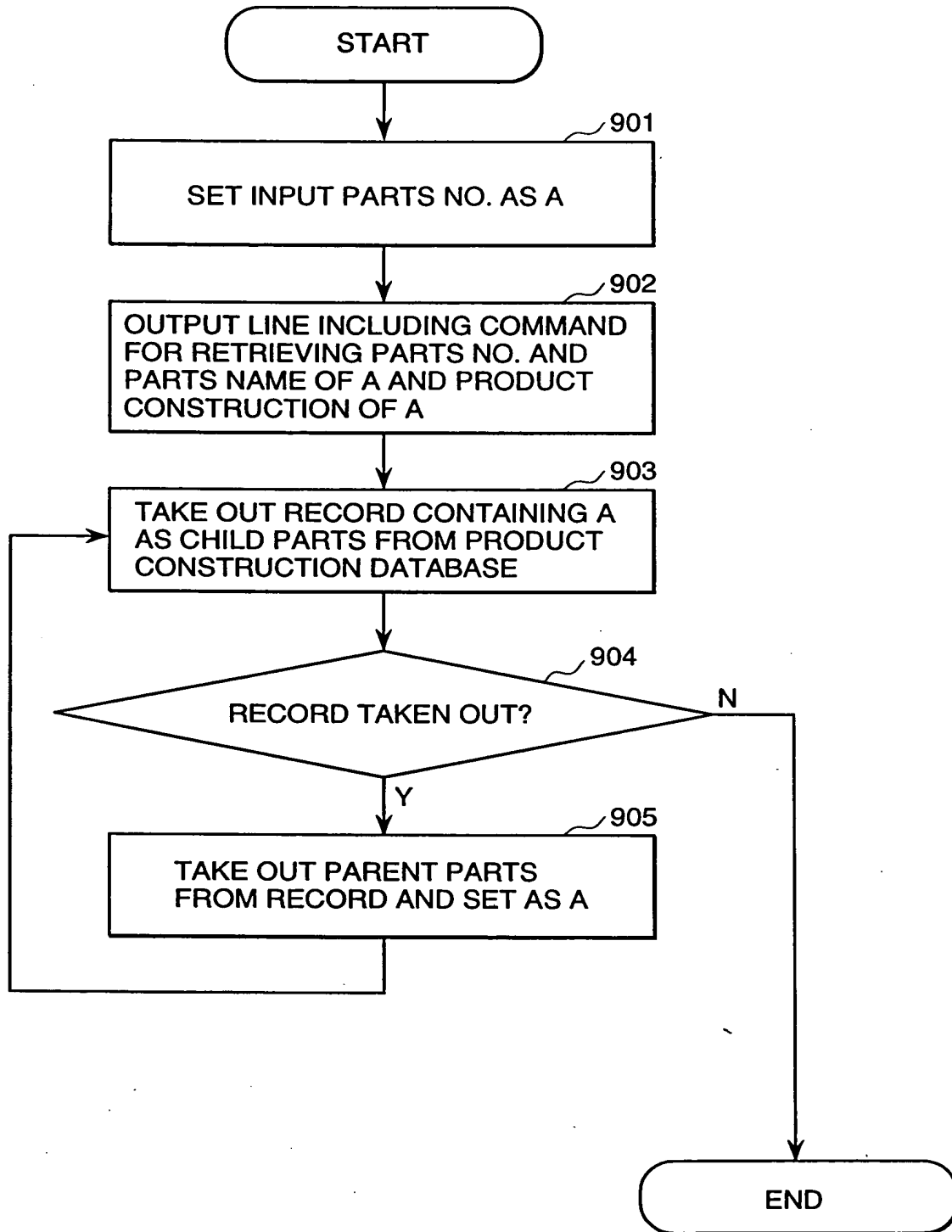
CLASSIFICATION	PARTS NAME		DRAWING	SPECIFICATION
C1	ELECTRONIC PARTS			
C2	CPU			
P4	X100 100MHz	<u>USAGE</u>	<u>D004</u>	<u>E004</u>
P15	X100 150MHz	<u>USAGE</u>	<u>D010</u>	<u>E010</u>
P20	X120 100MHz	<u>USAGE</u>	<u>D011</u>	<u>E011</u>

**FIG. 12**

1001	1002	1003	1004	1005
PARTS NO.	PARTS NAME		DRAWING	SPECIFICATION
P15	X100 150MHz	<u>PRODUCT CONSTRUCTION</u>	<u>D010</u>	<u>E010</u>
P12	CONTROL BOARD	<u>PRODUCT CONSTRUCTION</u>	<u>D007</u>	<u>E007</u>
P11	PRINTED BOARD ASSEMBLY	<u>PRODUCT CONSTRUCTION</u>	<u>D006</u>	<u>E006</u>

1006

**FIG. 10**

**FIG. 11**



*FIG. 13*

CLASSIFICATION	PARTS NAME/ CLASS NAME		DRAWING	SPECIFICATION
P11	PRINTED BOARD ASSEMBLY	<u>CLASS</u>	<u>D006</u>	<u>E006</u>
P12	CONTROL BOARD	<u>CLASS</u>	<u>D007</u>	<u>E007</u>
P14	CAPACITOR	<u>CLASS</u>		<u>E009</u>
P15	X100 150MHz	<u>CLASS</u>	<u>D010</u>	<u>E010</u>
P13	SPACER	<u>CLASS</u>	<u>D011</u>	

*FIG. 15*

CLASSIFICATION	PARTS NAME/ CLASS NAME		DRAWING	SPECIFICATION
C2	CPU			
P4	X100 100MHz	<u>CLASS</u>	<u>D004</u>	<u>E004</u>
P15	X100 150MHz	<u>CLASS</u>	<u>D010</u>	<u>E010</u>
P20	X120 100MHz	<u>CLASS</u>	<u>D011</u>	<u>E011</u>

FIG. 14

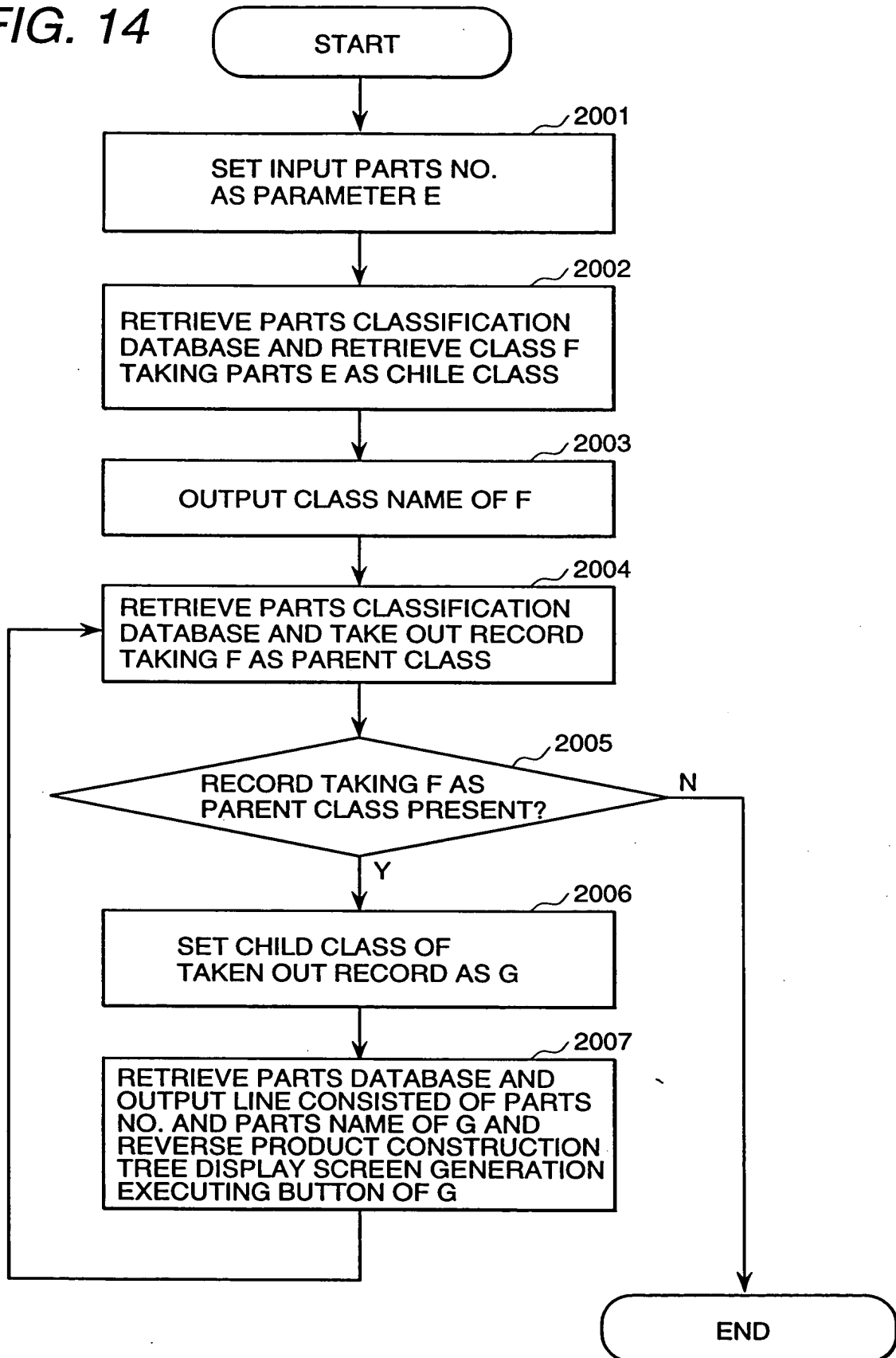


FIG. 16

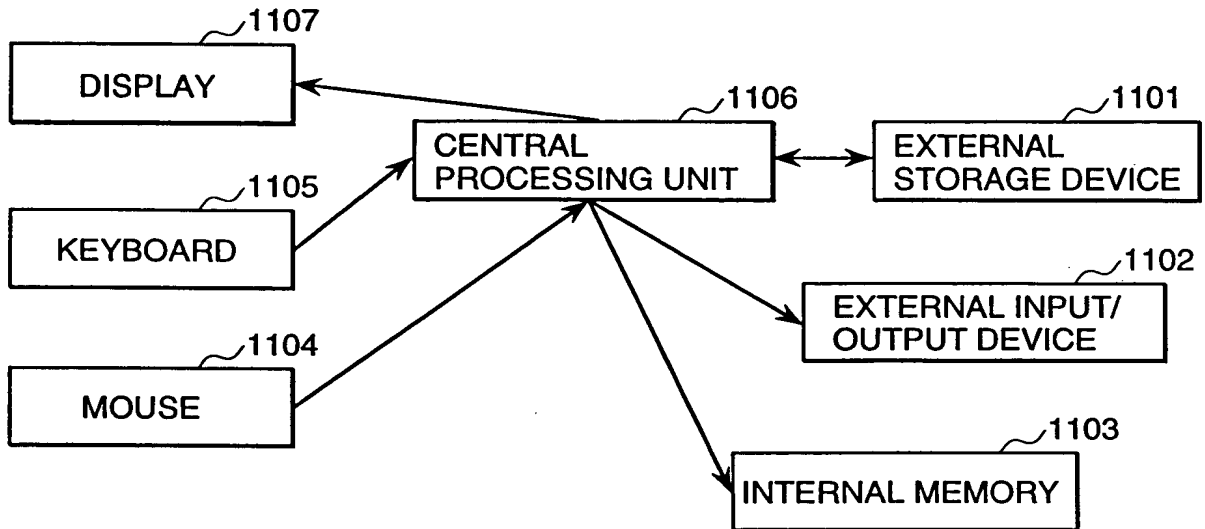


FIG. 17

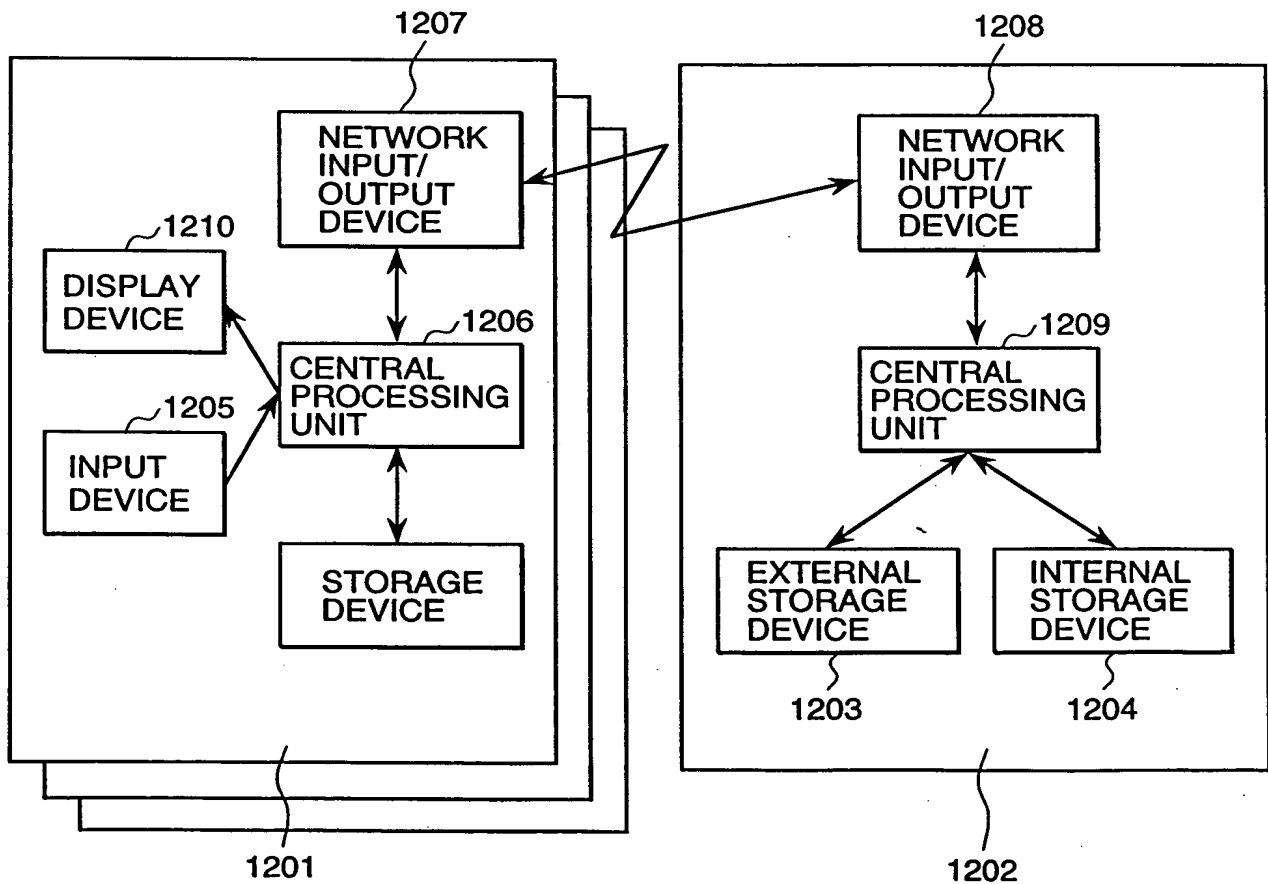
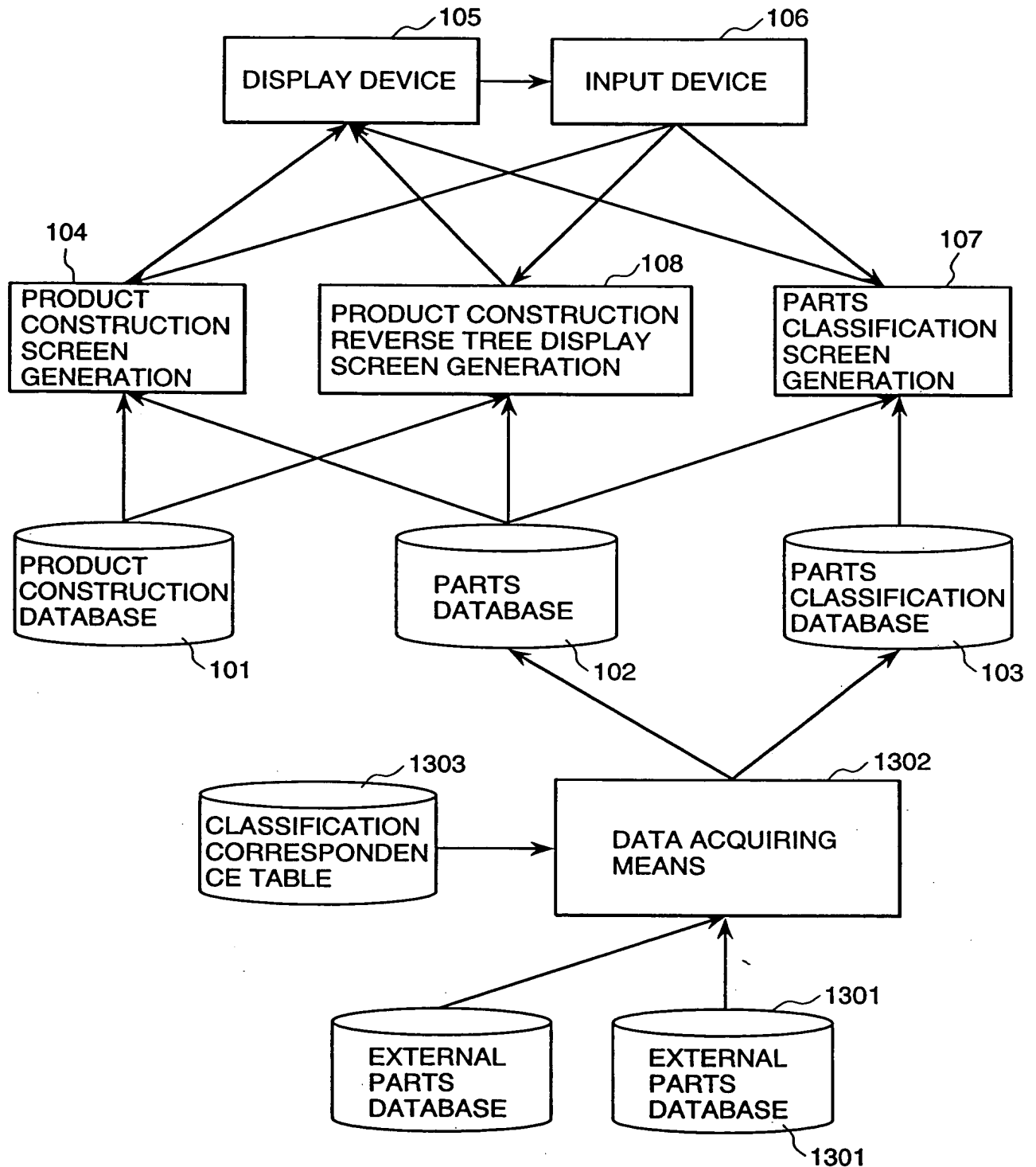


FIG. 18



*FIG. 19*

1401

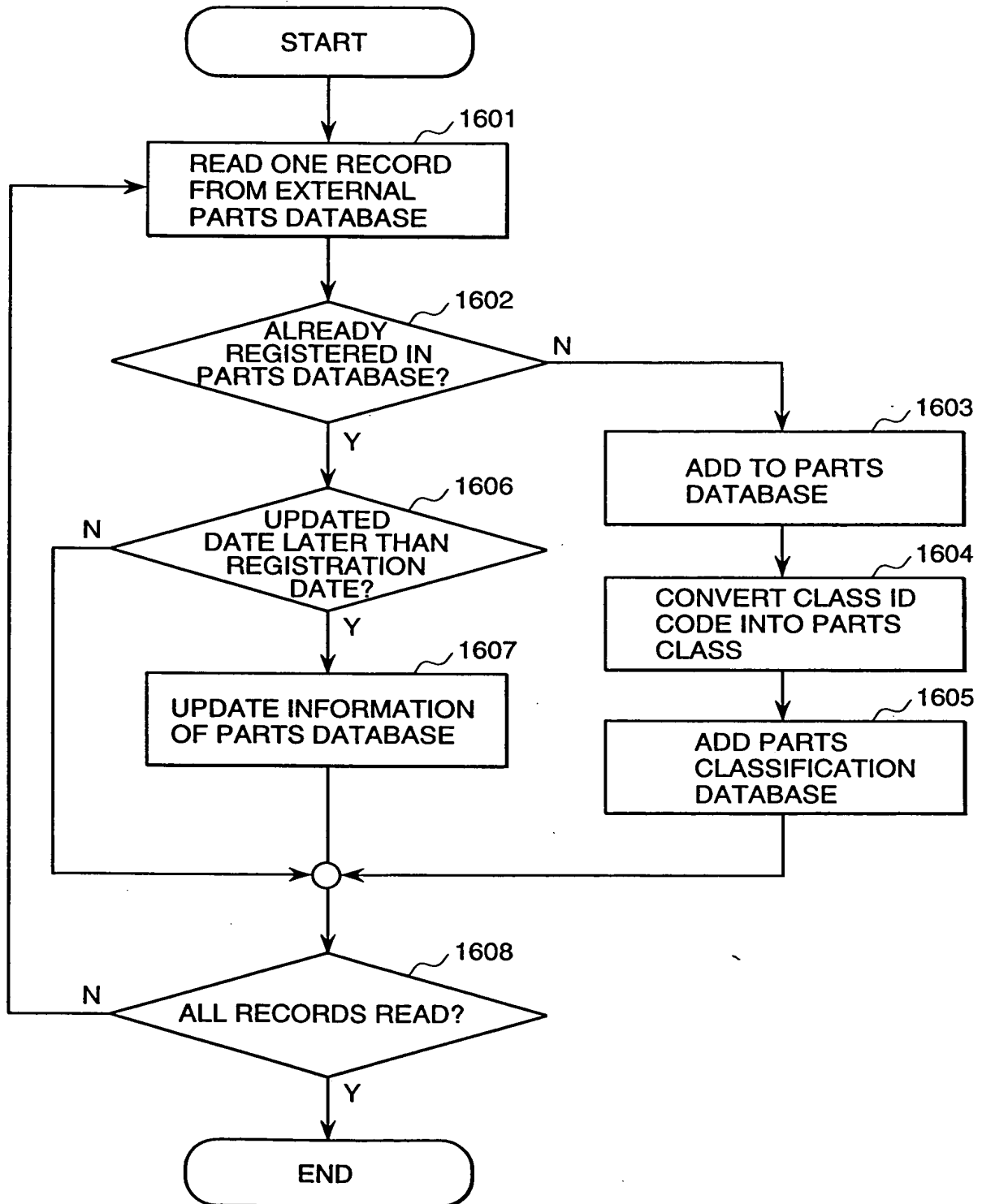
1402

CLASS ID CODE	PARTS NAME	PERFORMANCE (MIPS)	PRICE	MODIFICATION DATE
G11	X100 150MHz	150	¥1300	2001/07/21
G11	X110 100MHz	130	¥1500	2001/04/21
G11	X100 100MHz	100	¥1000	2001/04/21
G11	X120 150MHz	170	¥1500	2001/07/21
G11	X120 200MHz	250	¥2000	2001/07/21

*FIG. 20*

CLASS ID CODE	PARTS CLASS CODE
G11	C2
G12	C3

FIG. 21



**FIG. 22**

1701

PARTS NO.	PARTS NAME	DRAWING	SPECIFICATION	PRICE	REGISTRATION DATE
P1	PRINTED BOARD ASSEMBLY	D001	E001	¥1300	2001/05/21
P2	PRINTED BOARD	D002	E002	¥1500	2001/05/21
P3	SPACER	D003	—	¥1000	2001/05/21
P4	X100 100MHz	D004	E004	¥1500	2001/04/21
P5	CAPACITOR	—	E005	¥10	2001/01/21
P11	PRINTED BOARD ASSEMBLY	D006	E006	¥1300	2001/08/21
P12	CONTROL BOARD	D007	E007	¥1500	2001/08/21
P13	SPACER	D008	—	¥1000	2001/08/21
P14	CAPACITOR	—	E008	¥10	2001/01/21
P15	X100 150MHz	D010	E010	¥1500	2001/04/21
P20	X110 150MHz	D011	E011	¥1000	2001/04/21

**FIG. 23**

CLASSIFICATION	PARTS NAME	DRAWING	SPECIFICATION
C1	ELECTRONIC PARTS		
C2	CPU		
P4	X100 100MHz	<u>USAGE</u> D004	<u>E004</u>
P15	X100 150MHz	<u>USAGE</u> D010	<u>E010</u>
P20	X120 100MHz	<u>USAGE</u> D011	<u>E011</u>
P21	X150 150MHz	<u>USAGE</u> —	—
P22	X150 200MHz	<u>USAGE</u> —	—